# This Page Is Inserted by IFW Operations and is not a part of the Official Record

# **BEST AVAILABLE IMAGES**

Defective images within this document are accurate representations of the original documents submitted by the applicant.

Defects in the images may include (but are not limited to):

- BLACK BORDERS
- TEXT CUT OFF AT TOP, BOTTOM OR SIDES
- FADED TEXT
- ILLEGIBLE TEXT
- SKEWED/SLANTED IMAGES
- COLORED PHOTOS
- BLACK OR VERY BLACK AND WHITE DARK PHOTOS
- GRAY SCALE DOCUMENTS

# IMAGES ARE BEST AVAILABLE COPY.

As rescanning documents will not correct images, please do not report the images to the Image Problem Mailbox.



# UNITED STATES DEPARTMENT OF COMMERCE

#### **Patent and Trademark Office**

Address: COMMISSIONER OF PATENTS AND TRADEMARKS

Washington, D.C. 20231

APPLICATION NO. FILING DATE FIRST NAMED INVENTOR ATTORNEY DOCKET NO. 09/517,314 03/02/00 CHO C M4065.0223/F **EXAMINER** MM91/0214 Thomas J D Amico KANG. **ART UNIT** PAPER NUMBER Dickstein Shapiro Morin & Oshinsky LLP 2101 L Street NW Washington DC 20037-1526 2811 DATE MAILED: 02/14/01

Please find below and/or attached an Office communication concerning this application or proceeding.

**Commissioner of Patents and Trademarks** 

		11 1/0\	
	Application No.	Applicant(s)	
	09/517,314	CHO, CHIH-CH	EN
Office Action Summary	Examiner	Art Unit	
	Donahee Kang	2811	address
The MAILING DATE of this communication	nongite cover sh	neet with the correspondence	
The MAU ING DATE of this communication	appears on a	THE EDOM	
The MAILING DATE of this communication iod for Reply  A SHORTENED STATUTORY PERIOD FOR R THE MAILING DATE OF THIS COMMUNICATI  Extensions of time may be available under the provisions of 37 Carlot after SIX (6) MONTHS from the mailing date of this communicati  If the period for reply specified above is less than thirty (30) days  If NO period for reply is specified above, the maximum statutory  If NO period for reply within the set or extended period for reply will, by  Failure to reply within the set or extended period for reply will, by  Any reply received by the Office later than three months after the earned patent term adjustment. See 37 CFR 1.704(b).	ion.	mum of thirty (65) from the mailing date of	timely. this communication. 3).
earned patent term adjusts	on 29 January 2001 .		
tatus  1) Responsive to communication(s) filed (	This action is non-f	final.	to the merits is
1) Responsive to communication(s) filed (2a) This action is <b>FINAL</b> . 2b)  3) Since this application is in condition for this accordance with the practice	I his action is	formal matters, prosecution a	s to the mone
2a) This action is in condition fo	r allowance except to a	9, 1935 C.D. 11, 453 O.G. 21	0.
2a) This action is <b>FINAL</b> .  3) Since this application is in condition for closed in accordance with the practice	e unuer Ex Paris		
			'
Disposition of Claims	oplication.	l'an	
Disposition of Claims  4) Claim(s) 1-38 is/are pending in the ap  4a) Of the above claim(s) 33-38 is/are	withdrawn from conside	eration.	
40) Of the above Claim(3)			
Claim(s)is/are anowed.			
-3 01-i-(c) 1-32 is/are rejection.			
6)⊠ Claim(s) is/are objected to.  7)□ Claim(s) is/are objected to restrict	use election real	uirement.	
7) Claim(s) is/are objected to.  8) Claims are subject to restrict	tion and/or election 1941		
8) Claims			
Application Papers	ne Examiner.		
Application Papers  9) The specification is objected to by the specification is objected to be specification in the specification is objected to be specification.	a objected to by the Exa	aminer.	
10) The drawing(s) filed on is/aic	lod on is: a) □ a	approved b) disapproved.	
9) The specification is objected is/are 10) The drawing(s) filed on is/are 11) The proposed drawing correction files are declaration is objected	led on		
The eath of declaration			
12) The ball of design		U.C.C. \$ 119(a)-(d) Of	(f).
Priority under 35 U.S.C. § 119  13) Acknowledgment is made of a claim and the second acknowledgment is made of a claim and acknowledgment is made of a claim acknowledgment is made of ack	im for foreign priority un	nder 35 U.S.C. & 115(5)	
a) All b) Some * c) None of	f:	-n received	
a) All b) Some * c) None of 1. Certified copies of the prior 2. Certified copies of the prior	ity documents have bee	received in Application No	··
1. Certified copies of the prior	rity documents have be	en received in Application (No nents have been received in t T Rule 17.2(a)). rtified copies not received.	his National Stage
2. Certified copies of the prior	ies of the priority docum	nents have been reconstruction and the next that the next	
3 Copies of the certified cop	ternational Bureau (PC	read copies not received.	
application in our application in our trached detailed Office a	action for a list of the con	rity under 35 U.S.C. § 119(e)	
* See the attached detailed Office a	claim for domestic prior		
14) Acknowledgement			
			440) Boner No(s).
1		18) T Interview Summary (F	
h-months?		10/ 🚅	eur ybbiiografi ,
Attachment(s)  Attachment(s)  Figure of References Cited (PTO-892)	(PTO-948)	19) Notice of Information	eur Abbuogram
Poferences Cited (P10-092)	eview (PTO-948) -1449) Paper No(s)	19) Notice of Informal Pat 20) Other:	
Attachment(s)  15) Notice of References Cited (PTO-892)  16) Notice of Draftsperson's Patent Drawing Recommendation Disclosure Statement(s) (PTO-17)	eview (PTO-948) -1449) Paper No(s)	19) Notice of fillowing. 20) Other:	Part of Paper N

Art Unit: 2811

#### **DETAILED ACTION**

#### Election/Restrictions

1. Applicant's election without traverse of Group I (claims 1-32) in Paper No. 6 is acknowledged.

### **Drawings**

2. Figures 1-3 should be designated by a legend such as --Prior Art-- because only that which is old is illustrated. See MPEP § 608.02(g).

## Claim Rejections - 35 USC § 112

3. The following is a quotation of the first paragraph of 35 U.S.C. 112:

The specification shall contain a written description of the invention, and of the manner and process of making and using it, in such full, clear, concise, and exact terms as to enable any person skilled in the art to which it pertains, or with which it is most nearly connected, to make and use the same and shall set forth the best mode contemplated by the inventor of carrying out his invention.

4. Claims 17 & 26 are rejected under 35 U.S.C. 112, first paragraph, as containing subject matter which was not described in the specification in such a way as to reasonably convey to one skilled in the relevant art that the inventor(s), at the time the application was filed, had possession of the claimed invention. The recitation of "further comprising an array of said memory cells" can be interpreted as setting forth structure not supported by the specification. There is no support in the specification for an array of said memory cells as recited in claim 17. The recitation of "semiconductor die being electrically connected to said conductive connector" can be interpreted as setting forth structure not supported by the specification. There is no support in the specification for "semiconductor die being electrically connected to said conductive connector as recited in claim 26.

Art Unit: 2811

5. The following is a quotation of the second paragraph of 35 U.S.C. 112:
The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

- 6. Claims **11 & 25** are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.
- 7. Claims 11 & 25 recites the limitation "a conductive plug positioned within <u>an</u> <u>insulator layer</u>" in lines 15 & 6. There is insufficient antecedent basis for this limitation in the claim.

# Claim Rejections - 35 USC § 103

- 8. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
  - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 9. Claims **1, 3-8, 18, 20-21, & 23** are rejected under 35 U.S.C. 103(a) as being unpatentable over Hong et al (US 6,008,117).

Regarding claim 1, Hong et al discloses a semiconductor structure comprising (Fig.1H):

an insulator layer (12); a conductive plug (16) positioned within said insulator layer; a diffusion barrier layer (14) located on said insulator layer and surrounding said plug; a non-conductive layer (22) having an etched via at least partially over conductive

Art Unit: 2811

plug, and a conductive connector (38) formed in said via in electrical contact with said plug. See also Col.2, lines 41-61.

Hong et al does not expressly teach the diffusion barrier layer (14) used as an etch-stop layer. It is inherent that the diffusion barrier layer (14) taught by Hong et al prevents the etching of insulator layer (12) since the material of diffusion barrier layer (silicon nitride) is identical with claimed etch-stop layer.

It is conventional in the art to provide the etch-stop and would have been obvious to one of ordinary skill in the art at the time the invention was made to form etch-stop layers in order to prevent the etching of insulator layer.

Regarding claim 3, Hong et al discloses the etch-stop layer comprising a silicon nitride. See Col.2 line 54.

Regarding claims **4-6**, Hong et al does not teach the etch-stop layer comprises silicon carbide, silicon dioxide, and BLOK (mixture of silicon nitride and silicon carbide).

It would have been obvious to one having ordinary skill in the art at the time the invention was made to form the etch stop layer, *having the materials as claimed*, since it has been held to be within the general skill of a worker in the art to select a known material on the basis of its suitability for the intended use as a matter of obvious design choice. *In re Leshin*, 125 USPQ 416.

Regarding claims **7-8 & 20-21**, Hong et al discloses the non-conductive layer comprising a borophosphosilicated glass. See Col.2, line50.

Regarding claim **18**, Hong et al discloses a semiconductor device comprising (Fig.1H):

Art Unit: 2811

a conductive element (16); an etch-resistant layer (14) surrounding an upper portion of said conductive element; a non-conductive layer (22) over said etch resistant layer and having a via over said conductive element, said via extending down to a level of said conductive element and etch resistant layer; and a conductive material (38) located in said via, wherein said conductive material contacts said conductive element.

Hong et al does not expressly teach the diffusion barrier layer (14) used as an etch-stop layer. It is inherent that the diffusion barrier layer (14) taught by Hong et al prevents the etching of insulator layer (12) since the material of diffusion barrier layer (silicon nitride) is identical with claimed etch-stop layer.

It is conventional in the art to provide the etch-stop and would have been obvious to one of ordinary skill in the art at the time the invention was made to form etch-stop layers in order to prevent the etching of insulator layer.

Regarding claim 23, Hong et al discloses a semiconductor device further comprising a conductive layer (34) located in said via.

Claims 2, 9-16, 19, 22, & 24-32, are rejected under 35 U.S.C. 103(a) as being unpatentable over Hong et al in view of Admitted prior art (Fig. 1). 10.

Regarding claims 10-11, 19, & 24, Hong et al discloses a semiconductor device comprising (Fig.1H):

a conductive plug (16) positioned within an insulator; an etch-stop layer (14) deposited on said insulator and around said conductive plug; an intermediate nonconductive layer (22) provided over said etch stop layer and having an etched via over

Art Unit: 2811

said plug; and at least one conductive layer (38) in said via in electrical connection with said plug.

Hong et al does not teach an active region in a substrate. However, admitted prior art teaches in Fig.1 the active region (22), wherein the conductive plug is provided on active region.

It would have been obvious to one of ordinary skill in the art at the time the invention was made to provide active region (doped region) in substrate since the active region (doped region) provide an electrical connects with interconnect layer in semiconductor device.

Regarding claims **2, 12, & 15**, Admitted prior art teaches conductive layer comprise (Fig. 1):

a first conductive layer (12) formed in said etched via, said first conductive layer including a portion in contact with said conductive plug; and a second conductive layer (16) deposited over and in contact with said first conductive layer.

Regarding claims **13-14 & 28-29**, Hong et al discloses the non-conductive layer comprising a borophosphosilicated glass. See Col.2, line50.

Regarding claims **9,15-16, 22, & 30-31**, Admitted prior art teaches the first conductive layer and second conductive layer comprising one or more materials, such as aluminum, copper, and doped polysilicon. See page 2, lines 15-16.

Regarding claims **25**, Hong et al discloses a semiconductor device comprising (Fig.1H):

Art Unit: 2811

a conductive plug (16) positioned within an insulator; an etch-stop layer (14) deposited on said insulator and around said conductive plug; an intermediate non-conductive layer (22) provided over said etch stop layer and having an etched via over said plug; and at least one conductive layer (38) in said via in electrical connection with said plug.

Hong et al does not teach a connection region in a substrate. However, admitted prior art teaches in Fig.1 the connection region (22), wherein the conductive plug provide on active region and a first conductive layer (12) deposited in and in contact with etched via.

Regarding claims **27 & 32**, Admitted prior art teaches the connection region comprises a doped region (22) within substrate, wherein conductive plug  $(M_1)$  is located over connection region.

## Conclusion

11. Any inquiry concerning this communication or earlier communications from the examiner should be directed to **Donghee Kang** whose telephone number is 703-305-9147. The examiner can normally be reached on Monday through Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Tom Thomas can be reached on 703-308-2772. The fax phone numbers for the organization where this application or proceeding is assigned are 703-308-7722 for regular communications and 703-308-7722 for After Final communications.

Art Unit: 2811

Page 8

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703-308-0956.

DHK February 6, 2001

TOM THOMAS
SUPERVISORY PATENT EXAMINER